

Firmware Developers (All Levels)

Overview

Lumacron Technology delivers world-class solutions to the optical communications marketplace. We're expanding our European Headquarters and opening a new regional hub in North America, so we're seeking talented Firmware Developers at all levels to support R&D activities at both locations.

Reporting to the Firmware Team Lead, candidates will be responsible for the architecture, design and verification of FPGA sub-systems within Lumacron's flagship network monitoring products. The role is multi-disciplinary and includes development of FPGA IP Cores, full FPGA System-on-Chip RTL designs and verification testbenches. Senior roles may include mentoring of less experienced colleagues.

Role Requirements (new graduates may reference relevant course content)

- Qualified to degree level in Computer Science, Engineering or equivalent discipline.
- Experience of designing FPGA IP cores or sub-systems.
- Experience of the following;
 - Designing FPGA architectures from a technical specification.
 - Design with Xilinx and Intel FPGA and CPLD devices.
 - Using VHDL/Verilog HDL languages for FPGA design.
 - Implementing high speed, multi-frequency clocking architectures.
 - Designing (code entry to timing analysis) using Intel and/or Xilinx FPGA tool sets.
 - Using source/revision control tools.
 - Designing for high-speed optical interfaces.
 - Designing for high-speed SERDES interfaces (up to 28G).
 - Knowledge of revision control systems; CVS, Subversion, GIT or similar.
- Ability to work to tight timescales, individually and collaboratively.
- Ability to comply with the UK GOV National Security Vetting Procedures (see link below)
<https://www.gov.uk/government/organisations/united-kingdom-security-vetting>

Additional Beneficial Skills

- Experience developing software / embedded software for FPGA-based hardware systems.
- Specific experience of Telecommunication/Data Communication hardware products.
- Knowledge of Telecoms Framing and Packet structures (OTN, SONET/SDH, PDH, Ethernet).

Other Info

- Package: Competitive salary, optional pre-tax pension contribution, flextime, 25 days paid holiday (plus statutory holidays), performance-related bonus scheme.
- Primary Location: 4 Pitreavie Court, Pitreavie Business Park, Dunfermline, Fife, KY11 8UU, Scotland, UK.

How to Apply

- CV and short covering letter to careers@lumacron.com. Informal enquiries welcome via the same address.